A recent trend has seen FPGAs gain a variety of hard blocks. These offer improved performance and energy for regularly used operations. DSP blocks were added to FPGAs, over the last few generations of devices, to help accelerate filters and other multiply-accumulate type operations. In the most recent devices from Xilinx, the DSP48E1 primitive is highly configurable, on a cycle-by-cycle basis, and can perform a wide variety of operations. This talk will present the iDEA soft processor built around a DSP block, as well as discuss other novel ways to exploit such resources.